Input Vector i

12

State Reg

10

s

of

Output Vector o

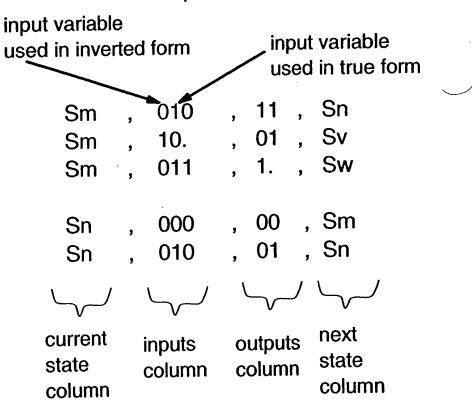combinational logic

Next State Vector

nf

Current State Vector

14

of = Output function
nf = Next State function

FIG. 1

FSM descriptions via State Tables

input variable
used in inverted form

input variable
used in true form

Sm	,	010	,	11	,	Sn
Sm	,	10.	,	01	,	Sv
Sm	,	011	,	1.	,	Sw

Sn	,	000	,	00	,	Sm
Sn	,	010	,	01	,	Sn

current
state
column

inputs
column

outputs
column

next
state
column

FIG. 2